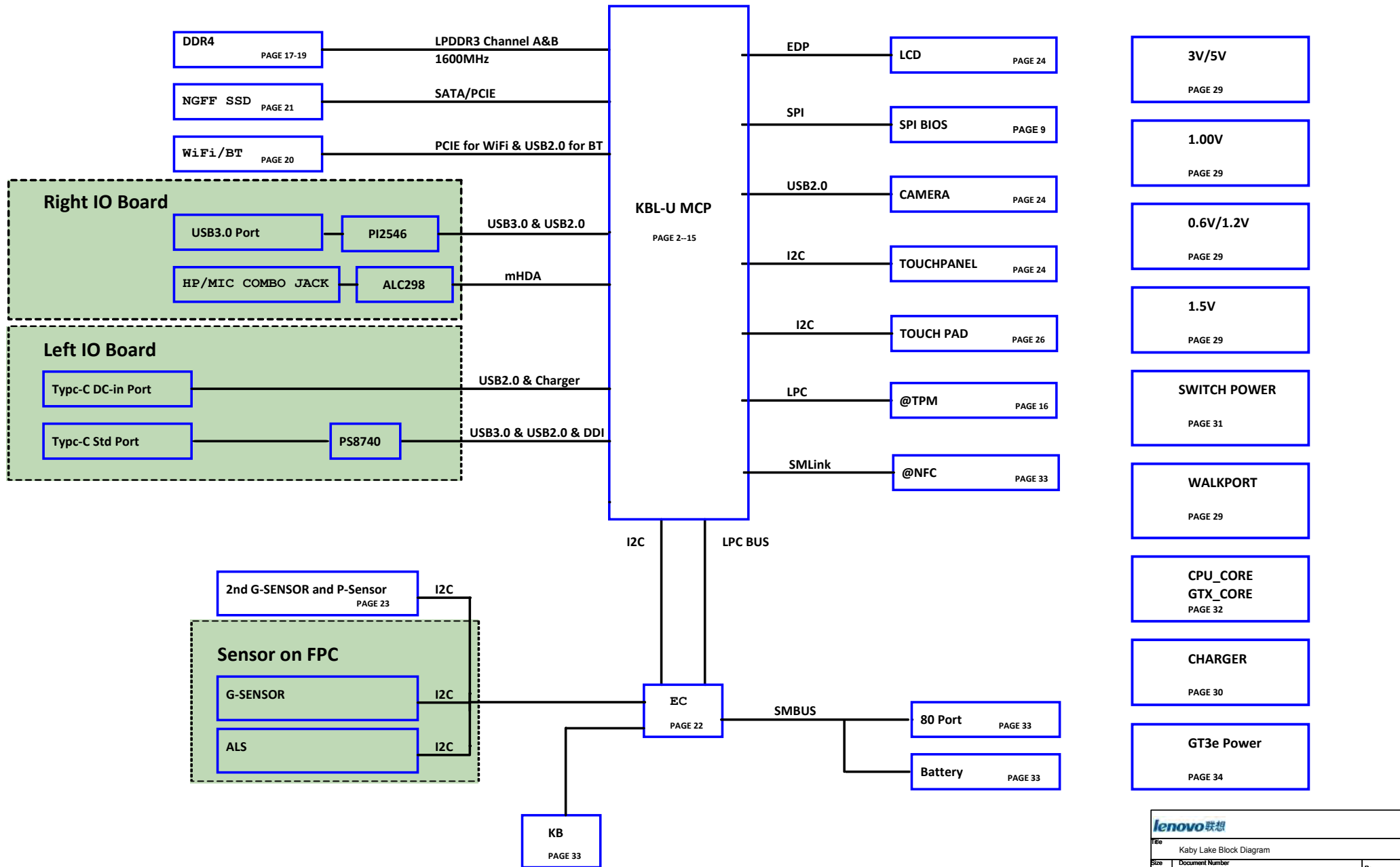
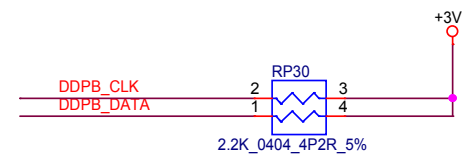
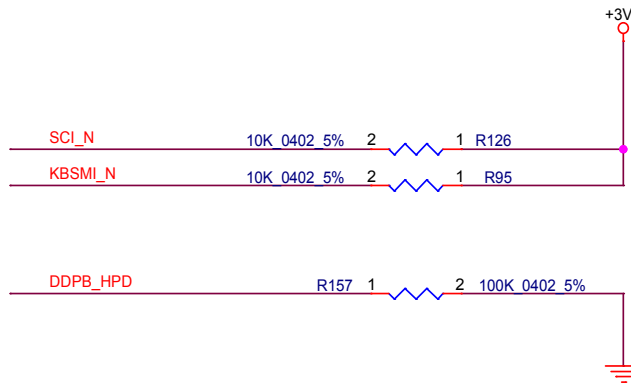
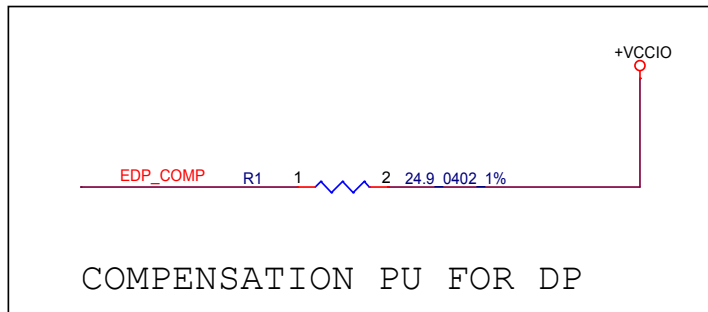
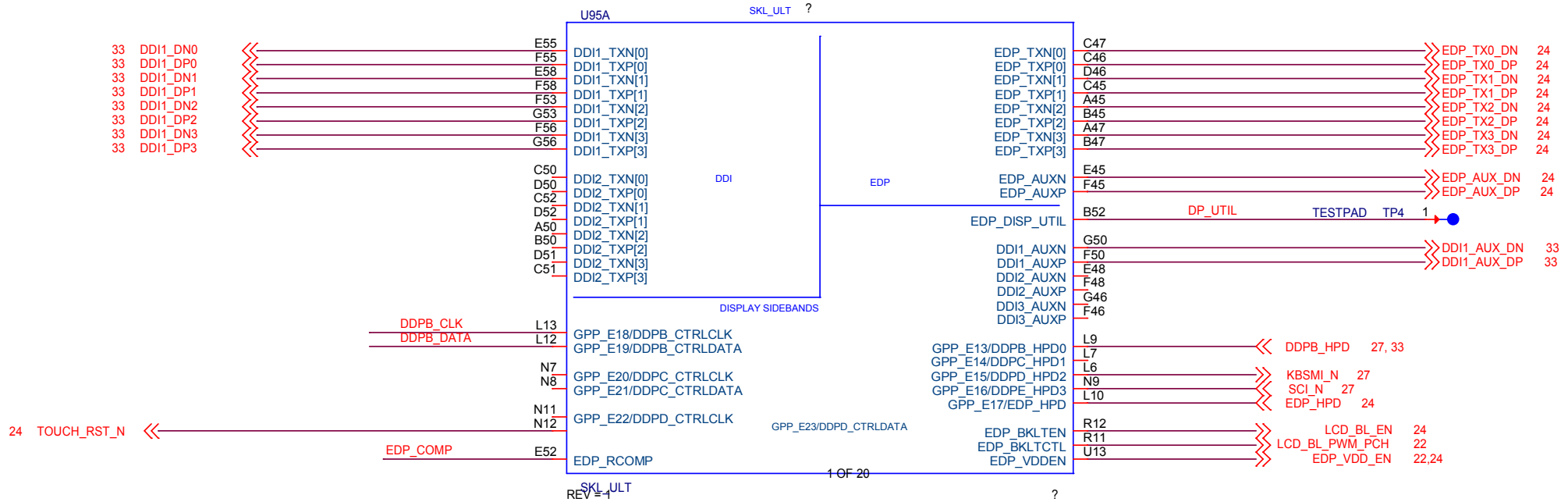
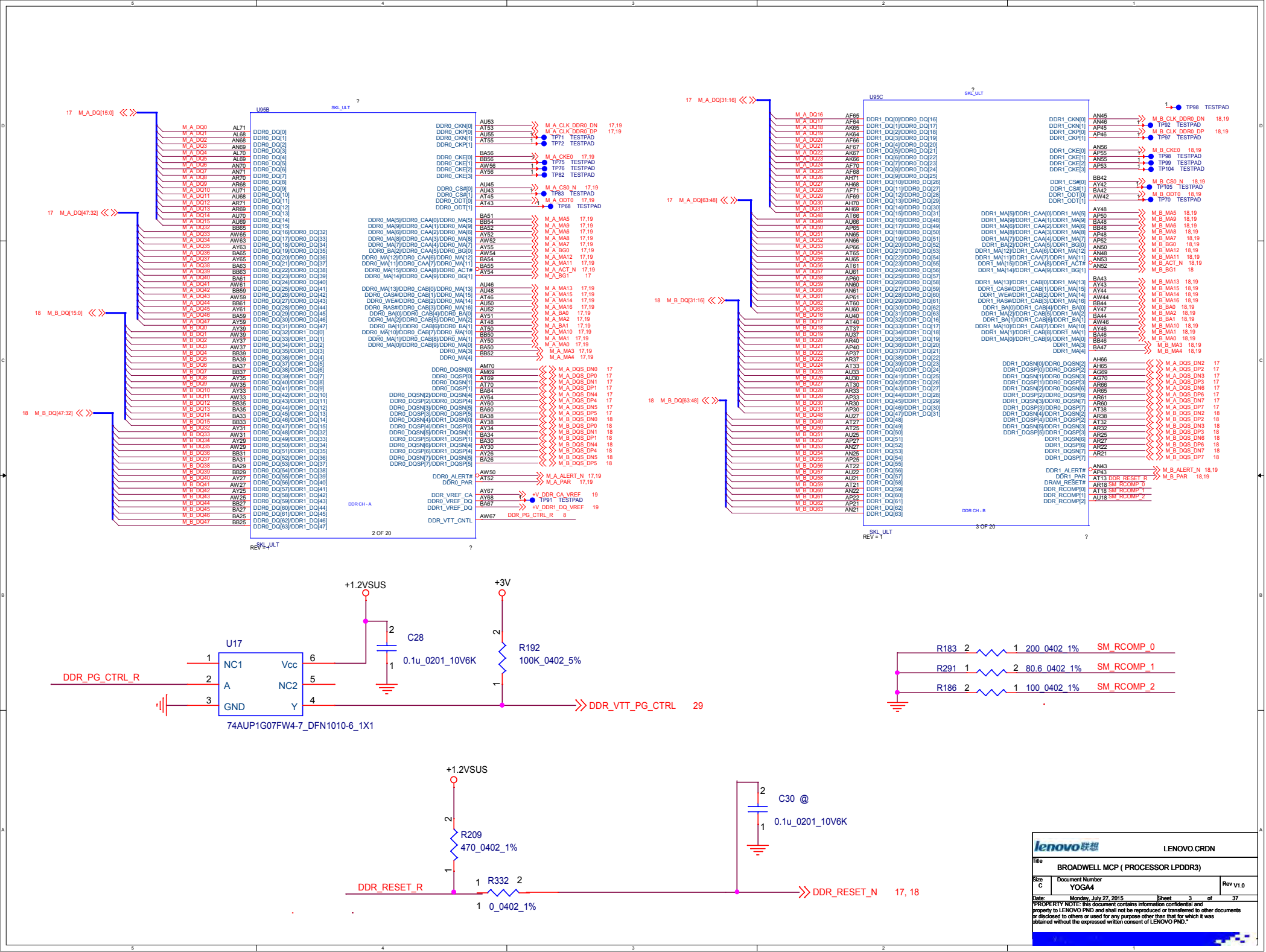
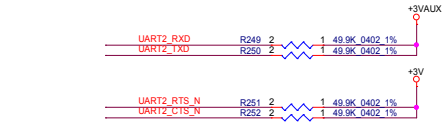
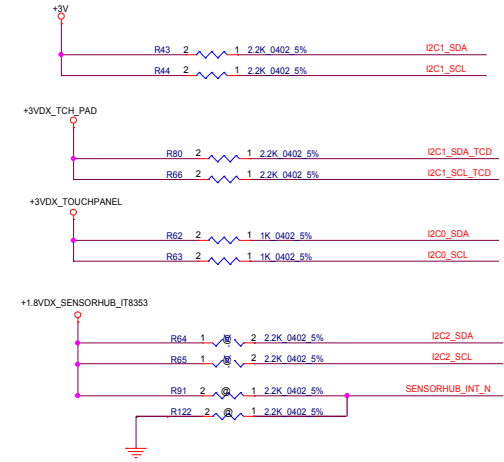
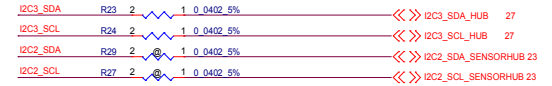
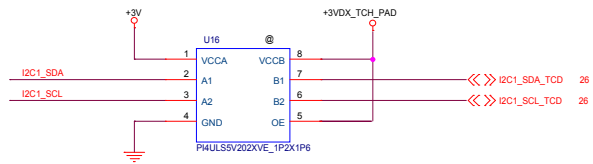
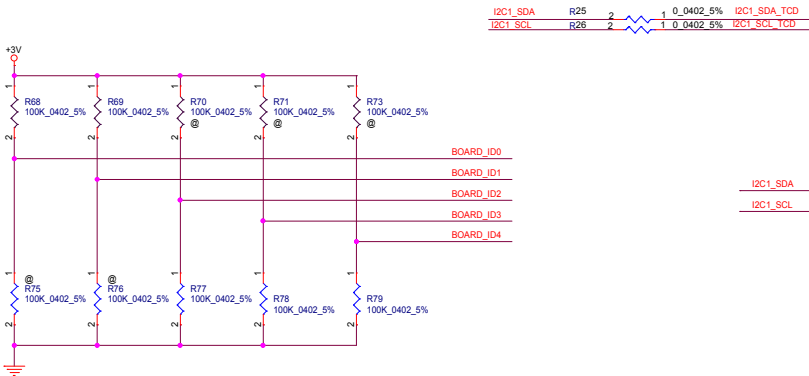
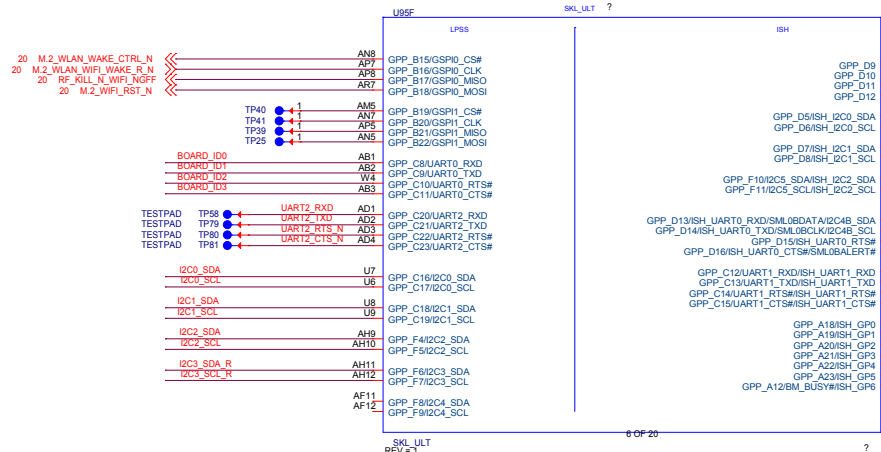


**Agera-SVT**  
**CYG50 NM-A901**  
**Schematic Block Diagram**







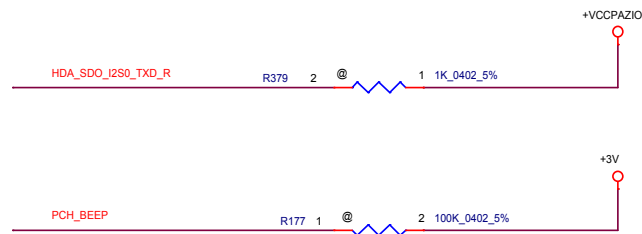
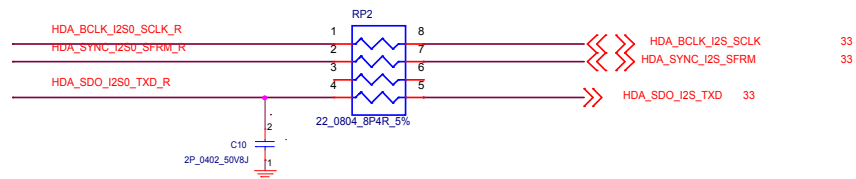
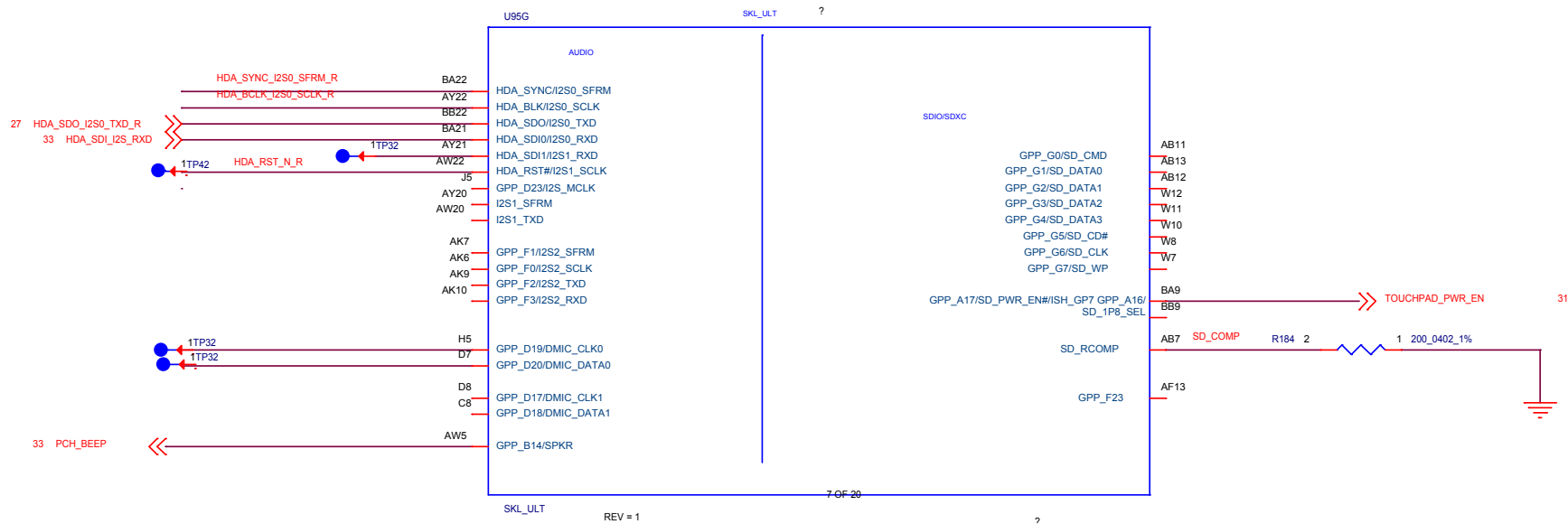


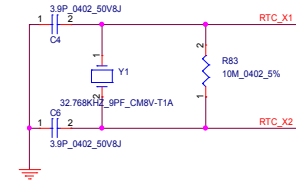
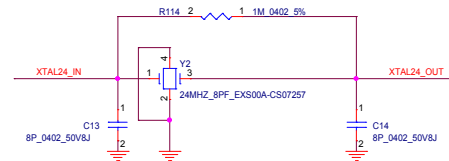
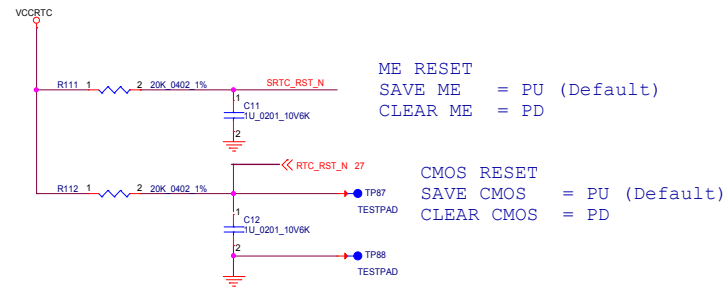
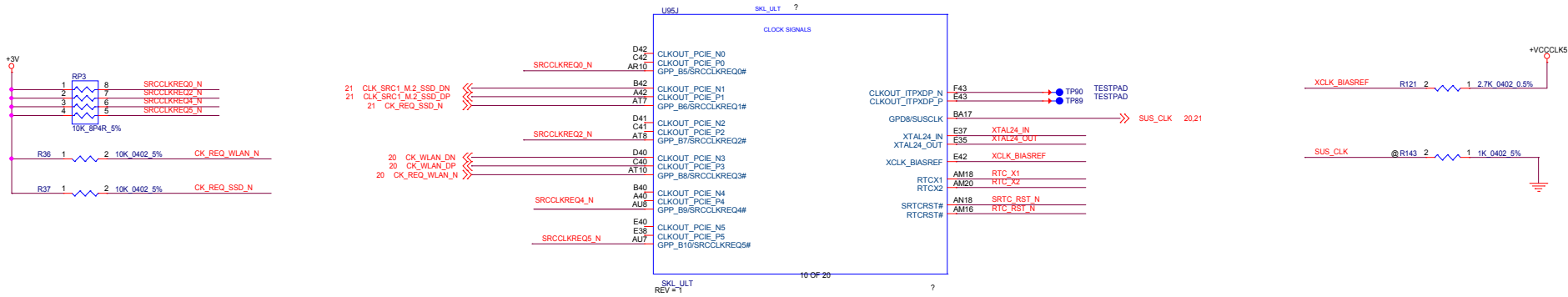
R163, R153	R580, R581, R146, R154	R183	R571, R572, R574, R575, R576, R577, R578, R579	BOARD_ID4	BOARD_ID3	BOARD_ID2	BOARD_ID1	BOARD_ID0	Description
Stuff	Dummy @	200ohm	0ohm	0	0	0	0	0	Samsung 8Gb x16 DDR4 2133
Stuff	Dummy @	200ohm	0ohm	0	0	0	0	1	Samsung 4Gb x16 DDR4 2133
Dummy @	Stuff	121ohm	240ohm	0	0	0	1	0	Micron 16Gb x16 DDR4 2133
Stuff	Dummy @	200ohm	0ohm	0	0	0	1	1	Micron 8Gb x16 DDR4 2133
Stuff	Dummy @	200ohm	0ohm	0	0	1	0	0	Samsung 8Gb x16 DDR4 2400
Stuff	Dummy @	200ohm	0ohm	0	0	1	0	1	Samsung 4Gb x16 DDR4 2400
Dummy @	Stuff	121ohm	240ohm	0	0	1	1	0	Micron 16Gb x16 DDR4 2400
Stuff	Dummy @	200ohm	0ohm	0	0	1	1	1	Micron 8Gb x16 DDR4 2400
Dummy @	Stuff	121ohm	240ohm	0	1	0	0	0	Samsung 16Gb x16 DDR4 2400

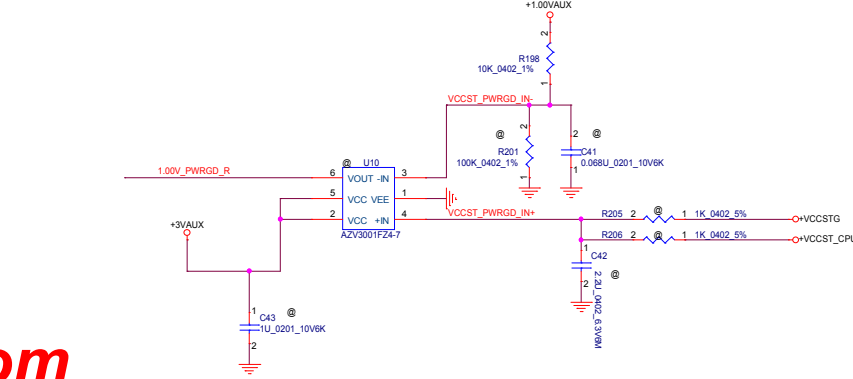
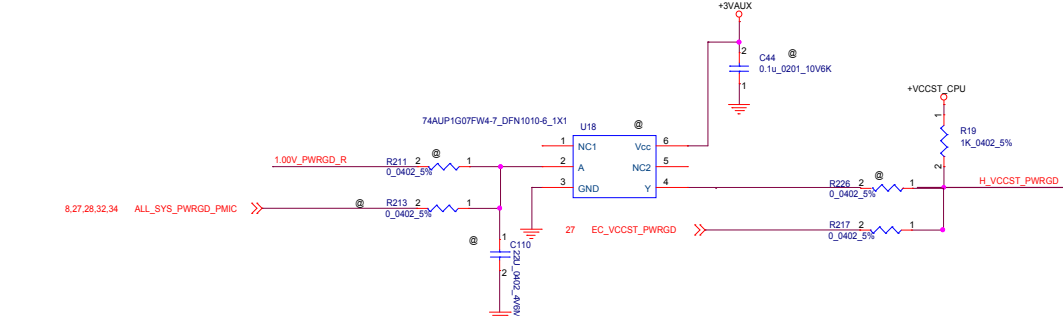
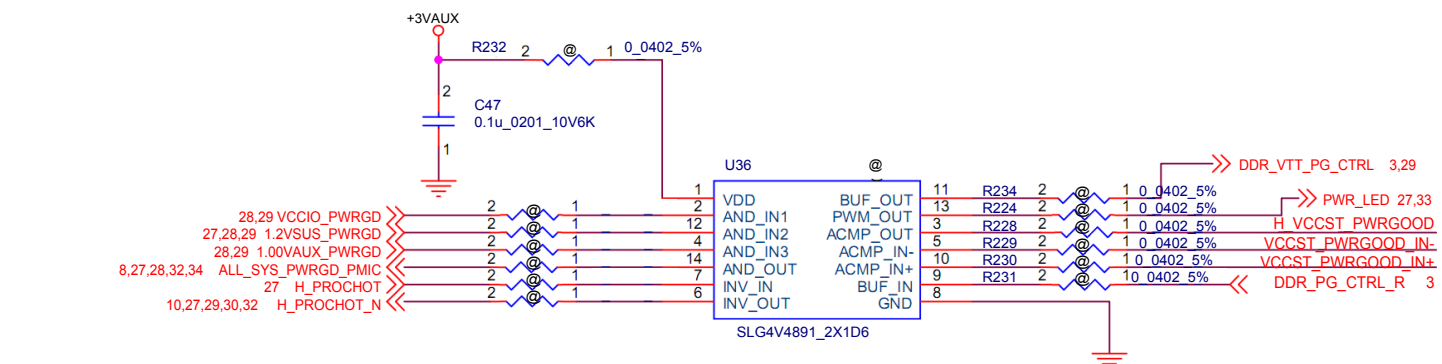
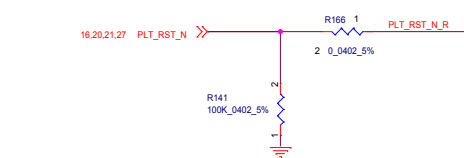
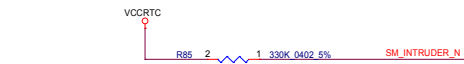
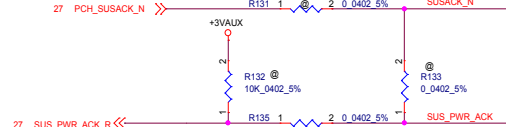
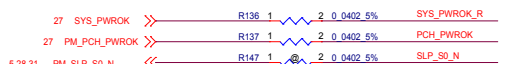
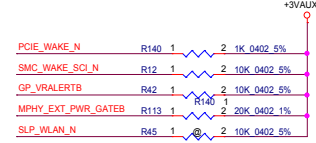
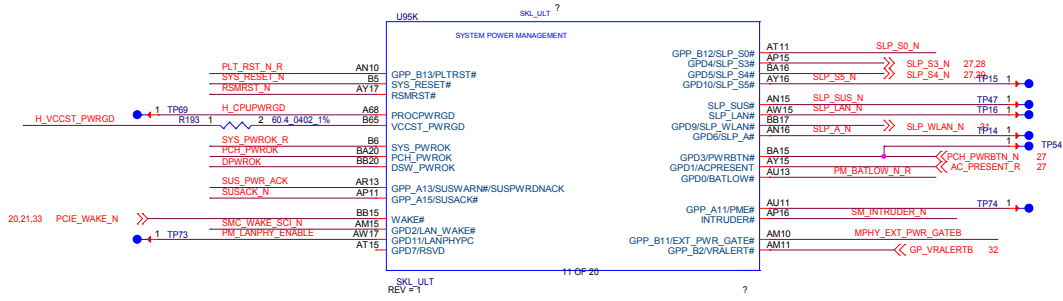
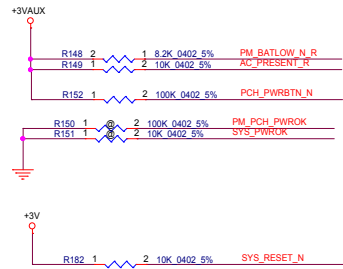
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BROADWELL MCP (PCIe USB GPIO)		
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Date	Monday, July 27, 2015	Sheet 4 of 37
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BROADWELL MCP (PM)

YOGA4

Rev 'v1.0

Monday, July 27, 2015

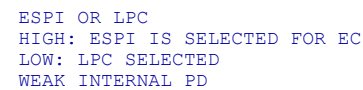
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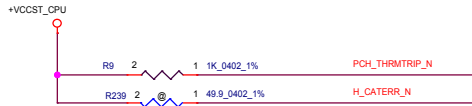
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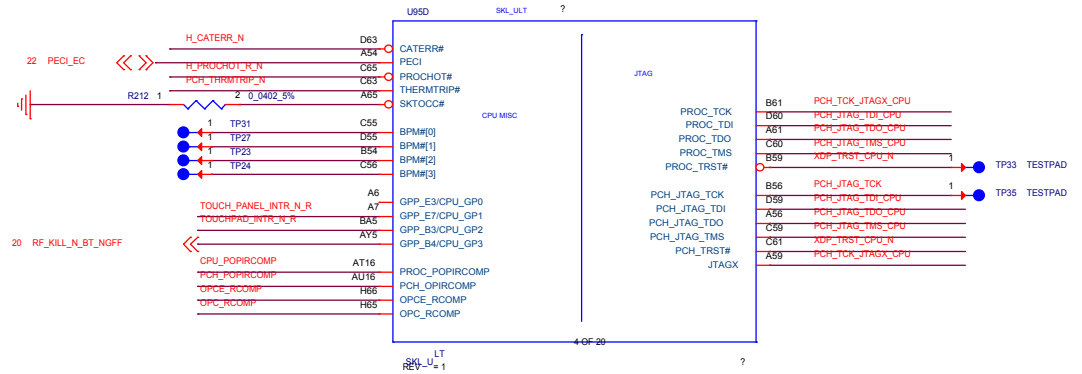
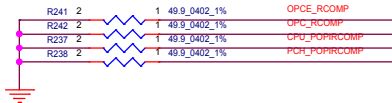
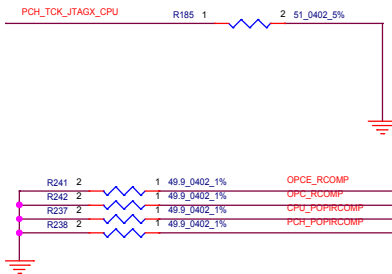
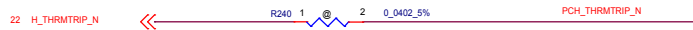
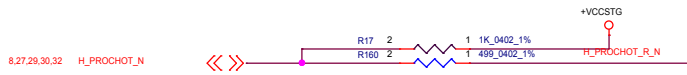
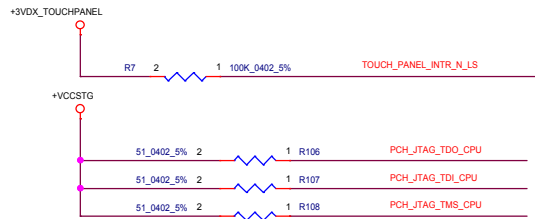
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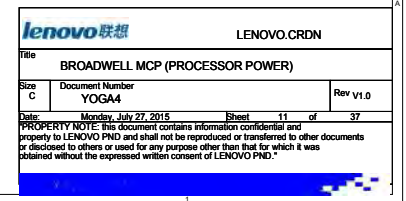


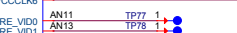
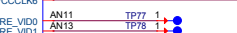
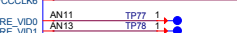
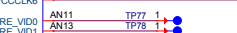
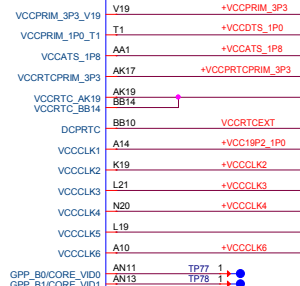
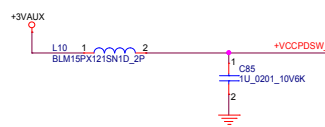
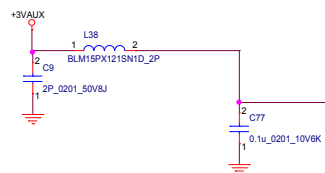
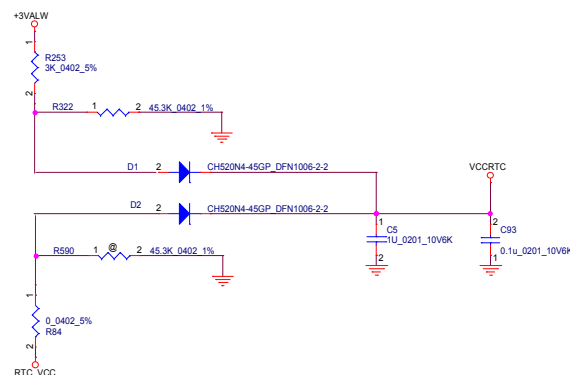
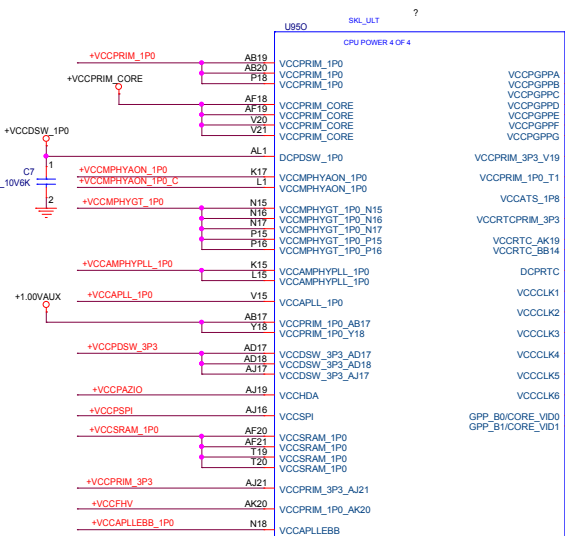
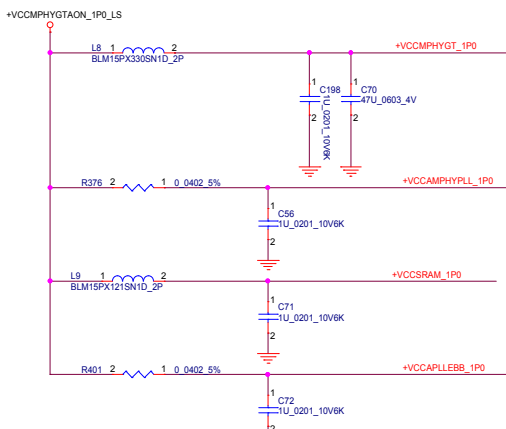
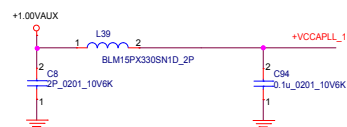
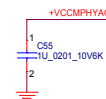
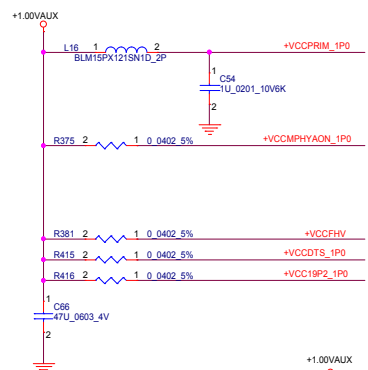
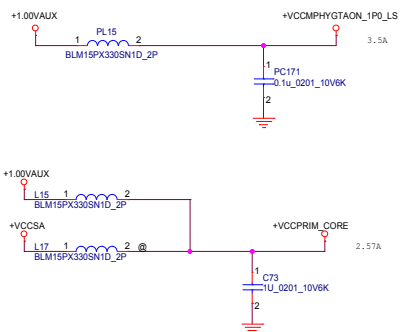


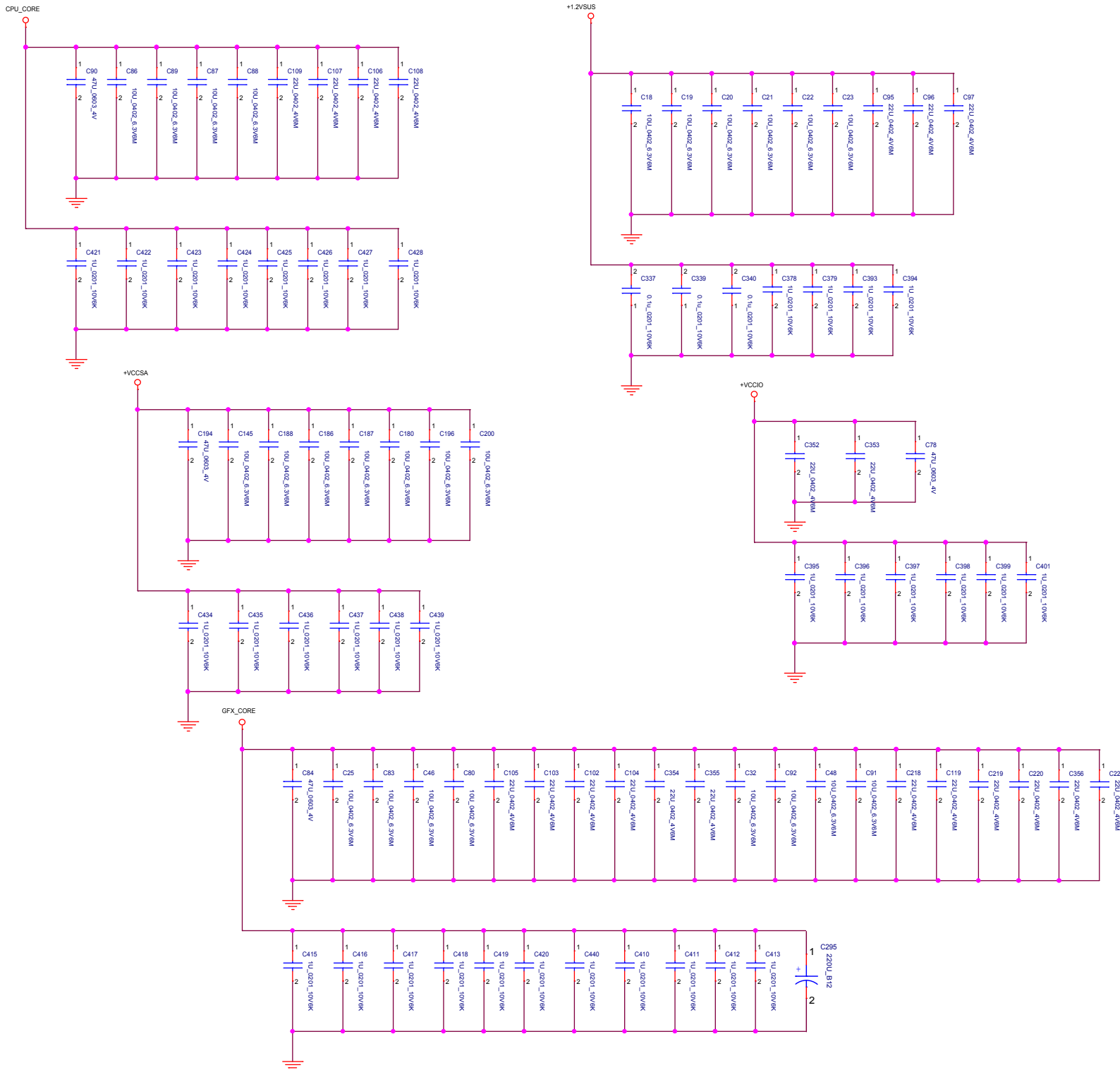


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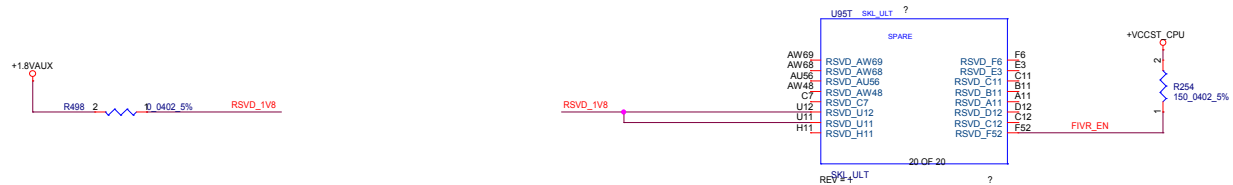


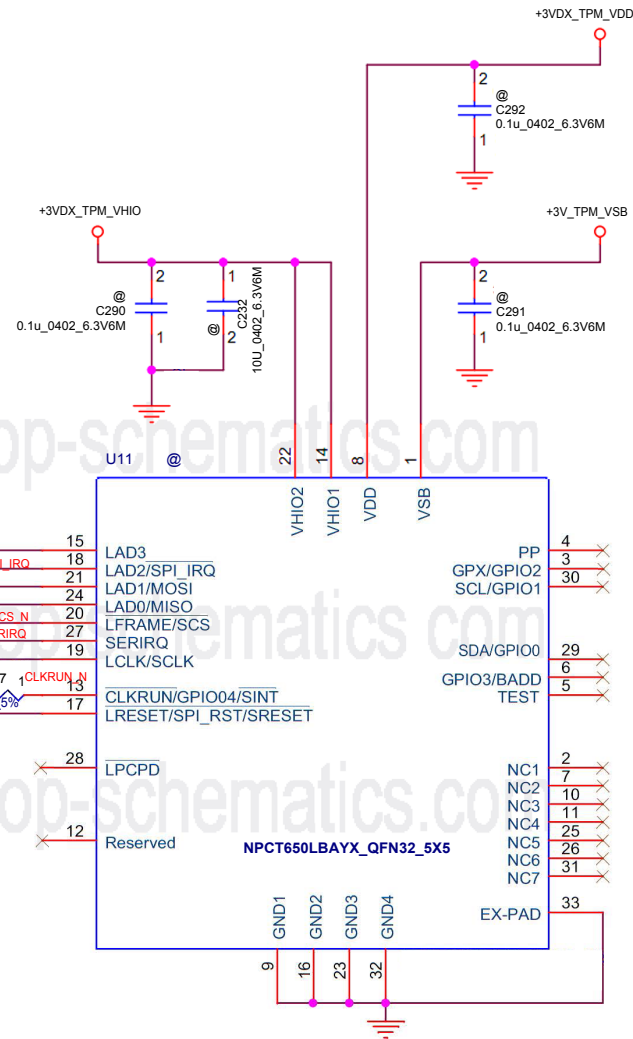
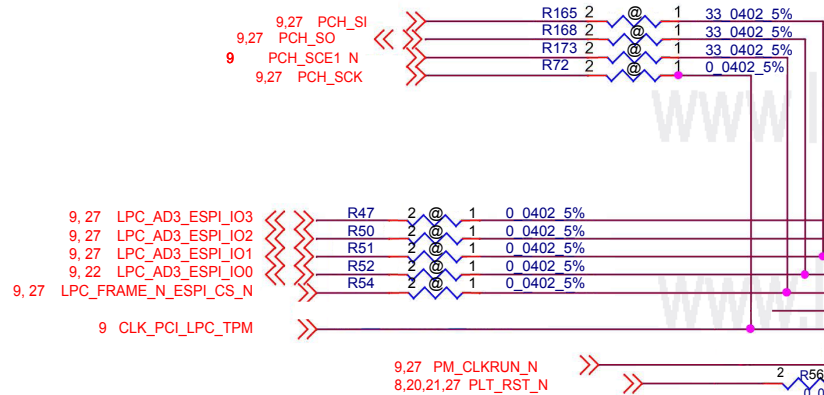
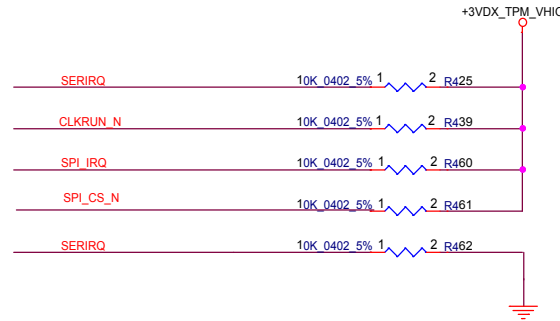
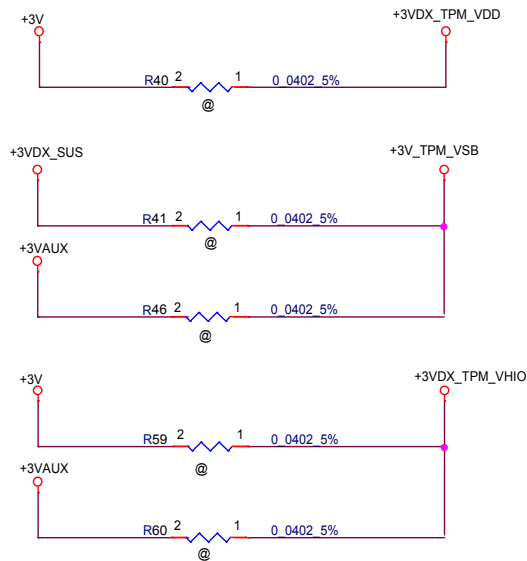






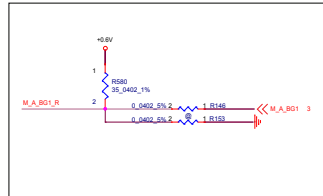




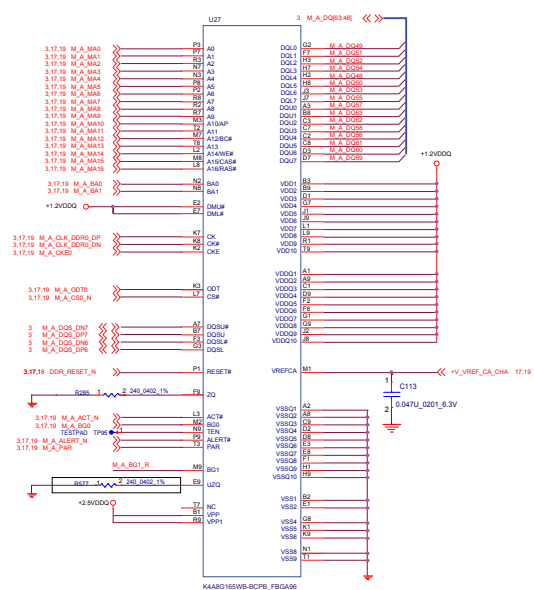


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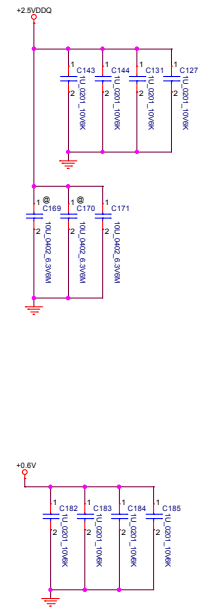
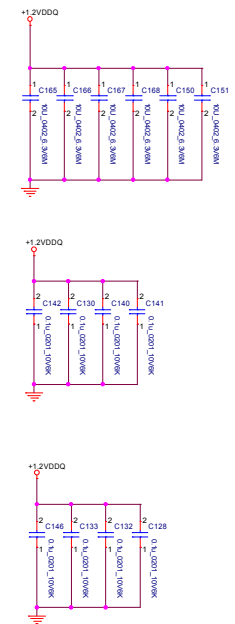
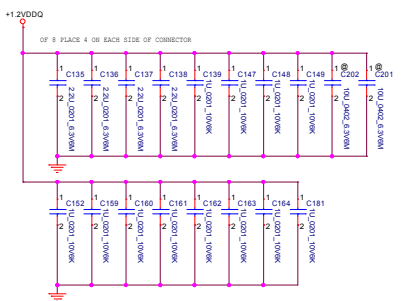
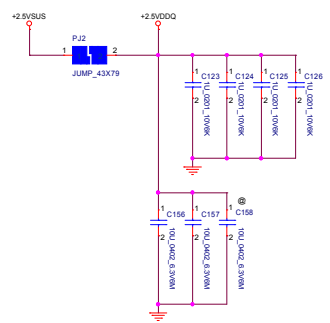
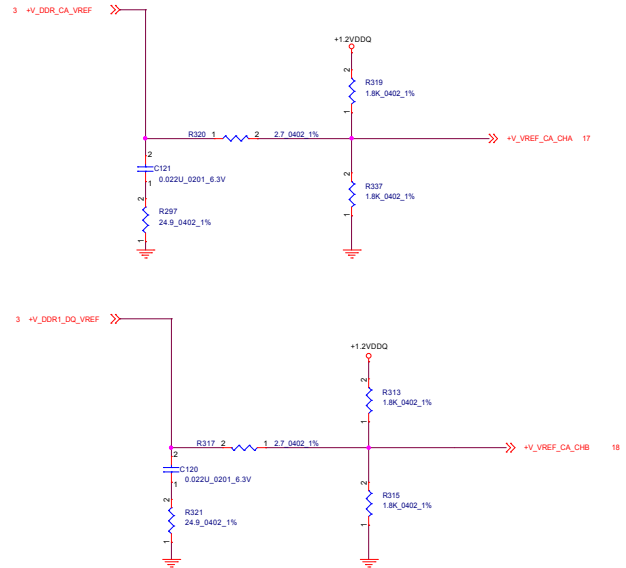
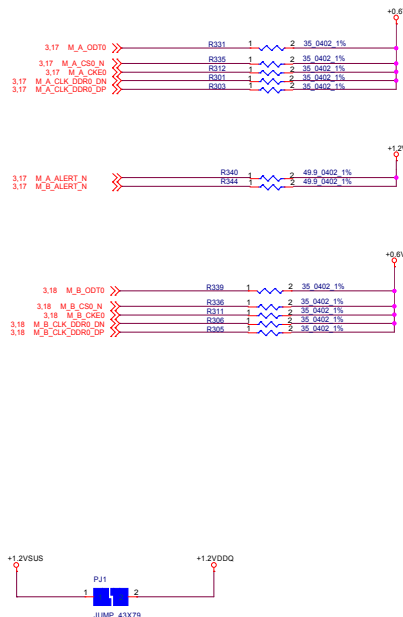
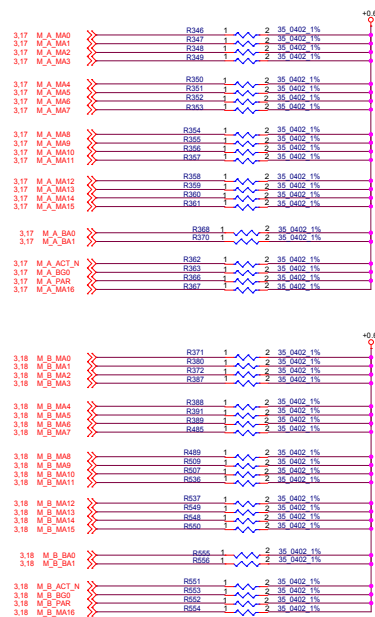


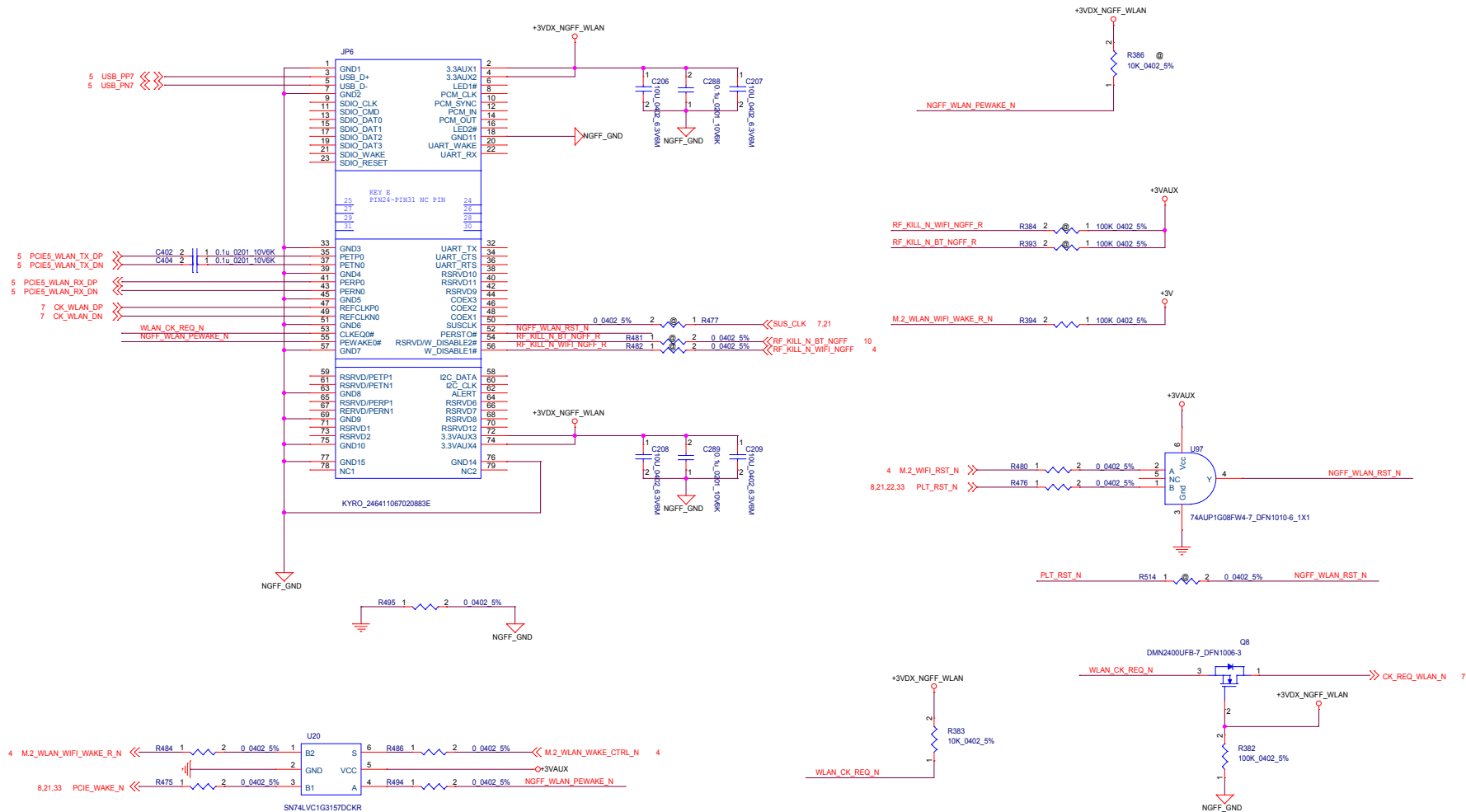


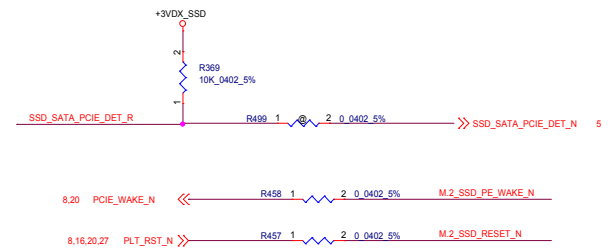
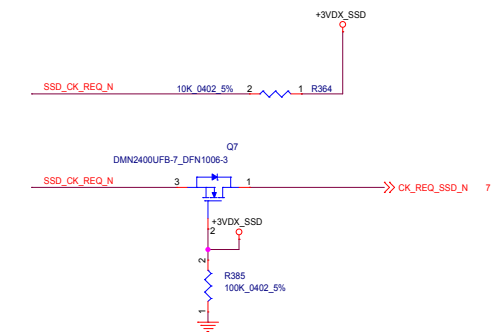
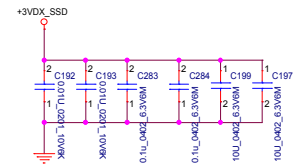
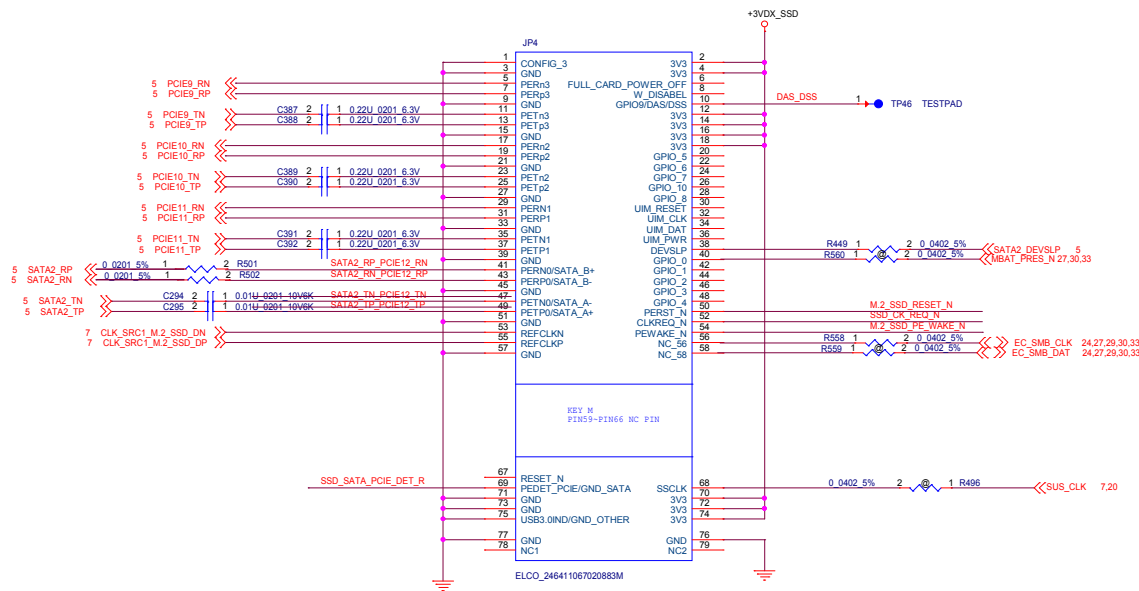
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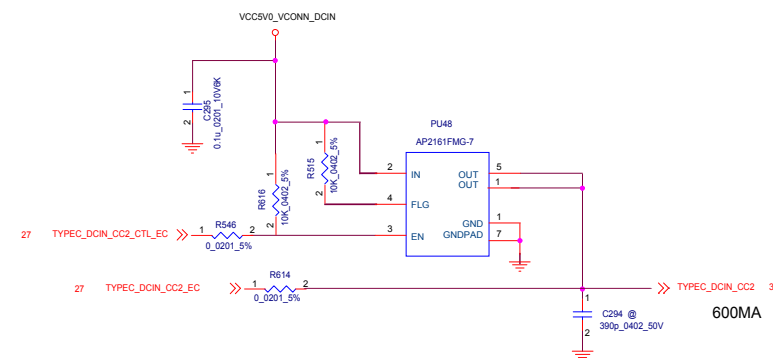
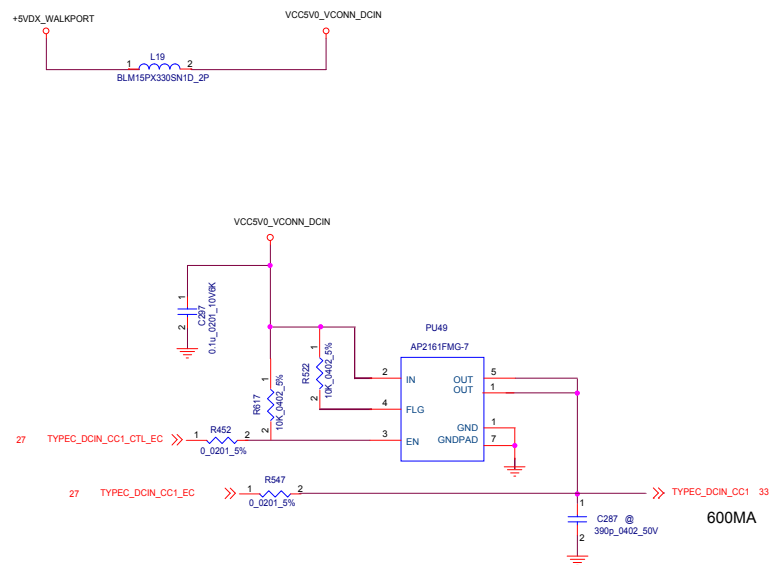
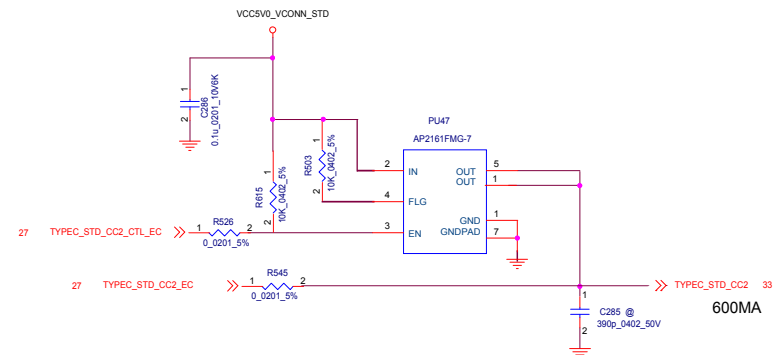
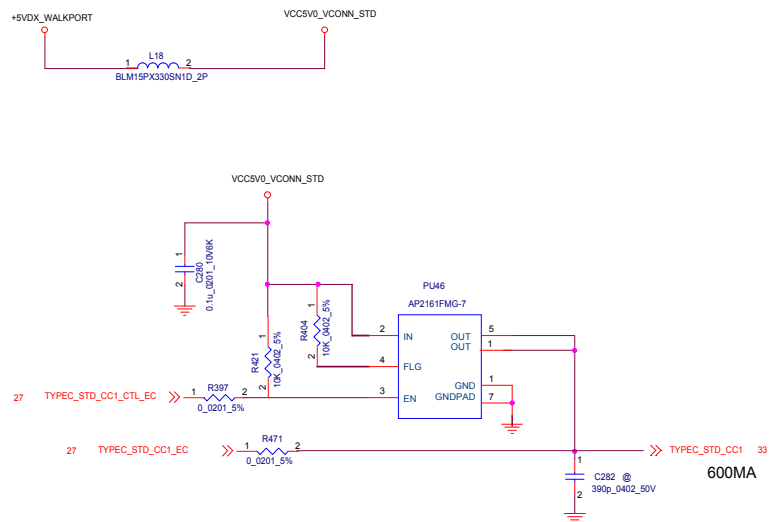


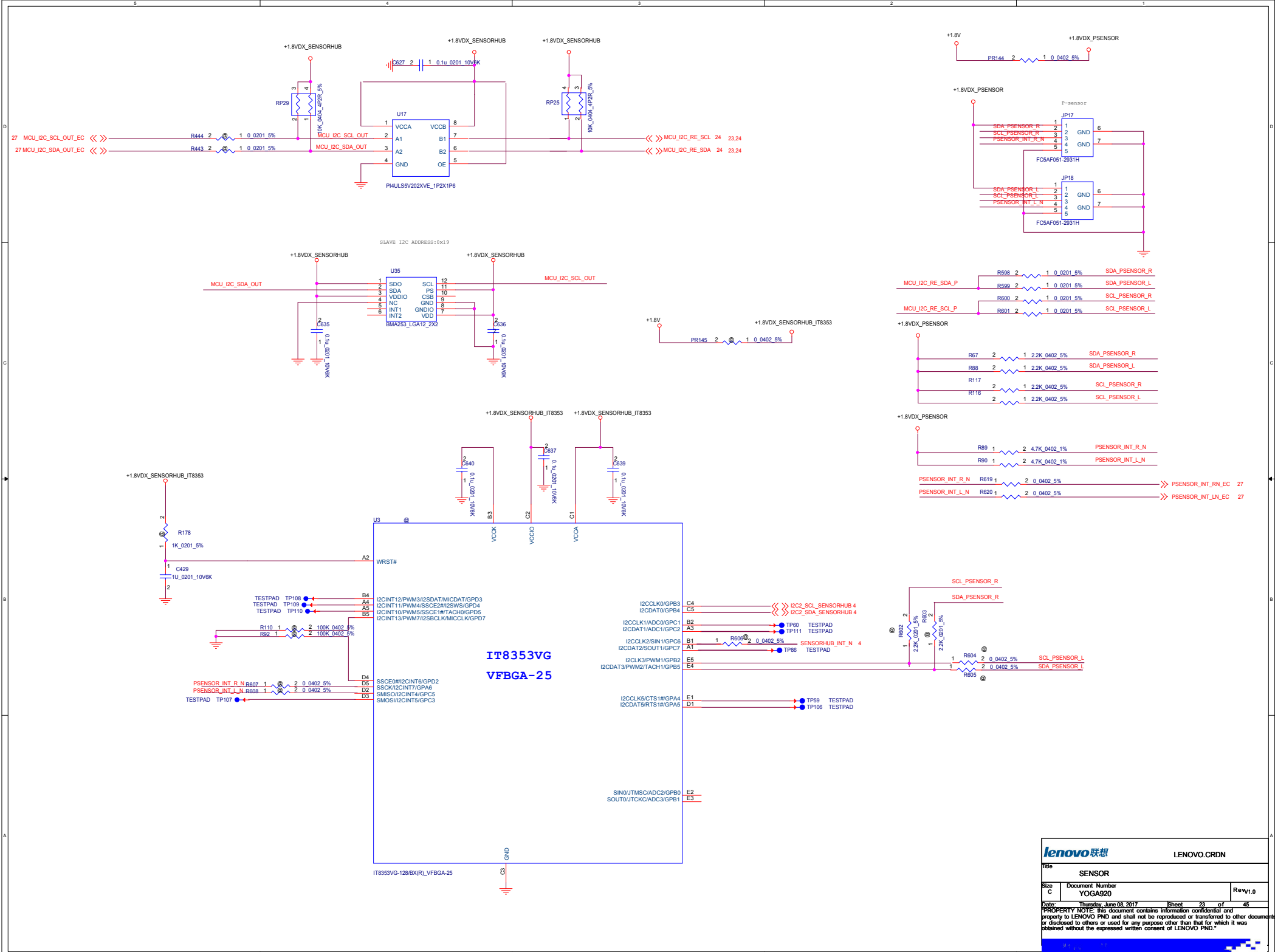


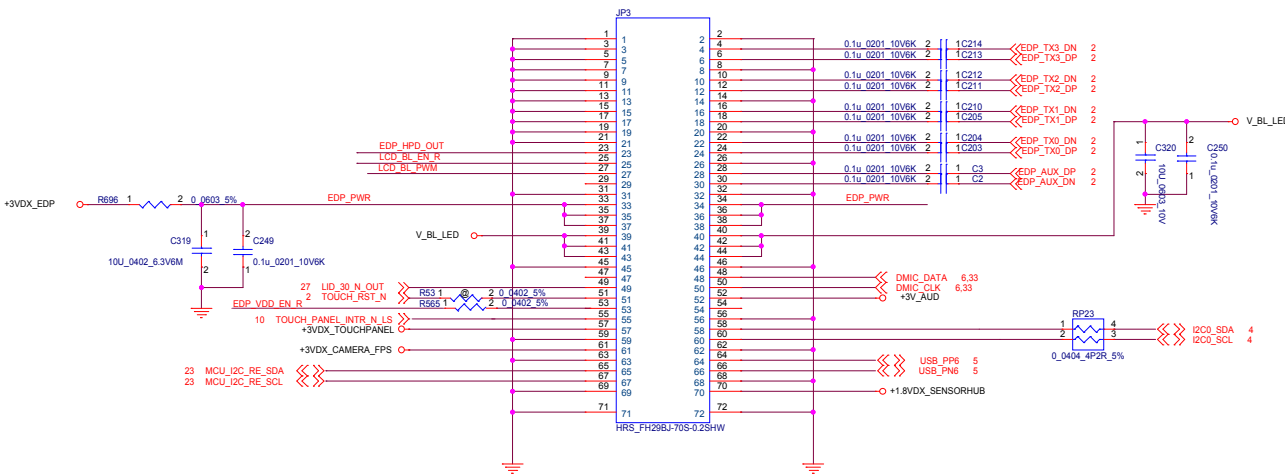
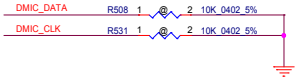
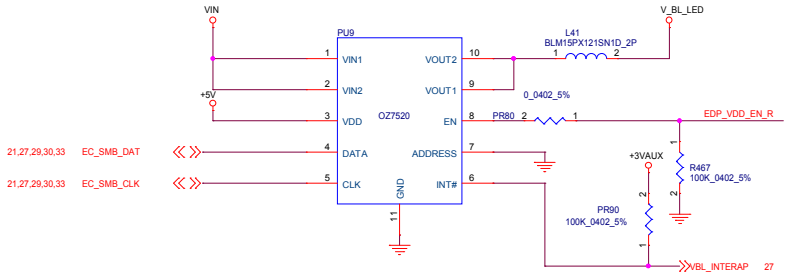
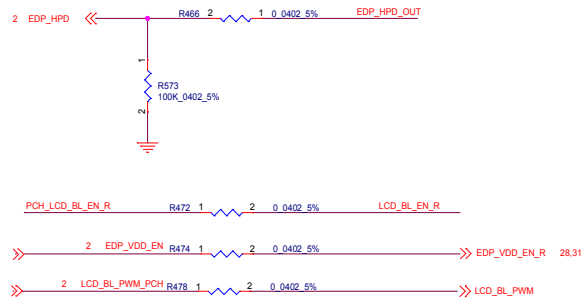
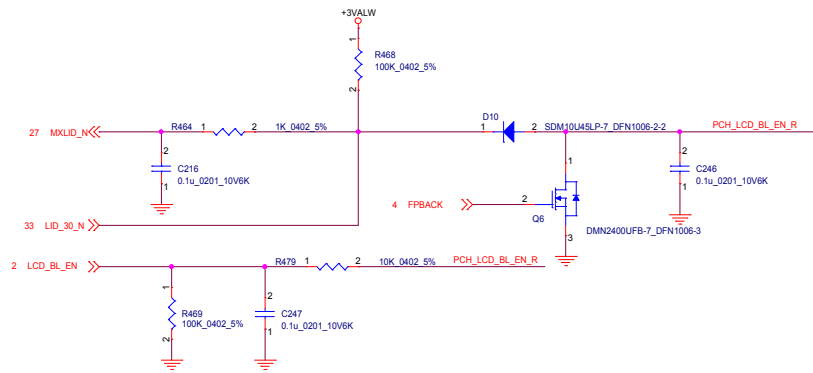








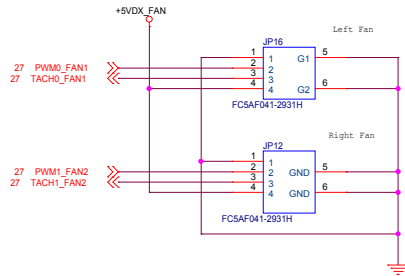
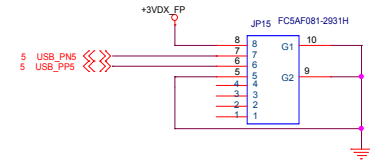
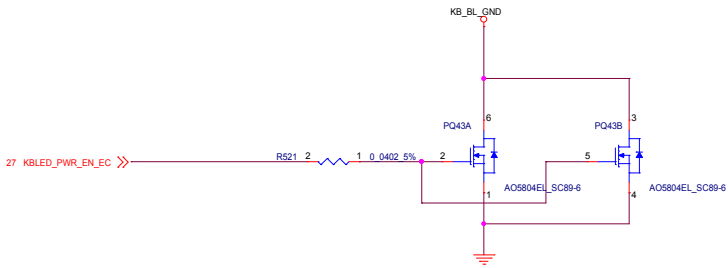
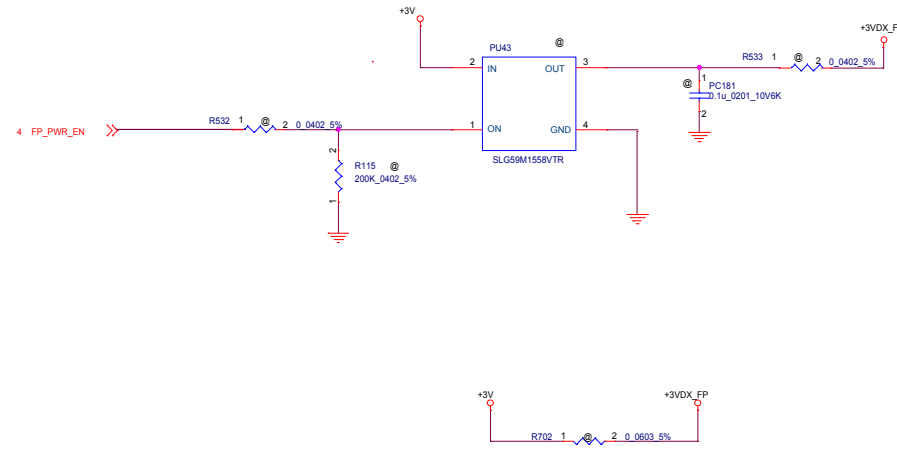
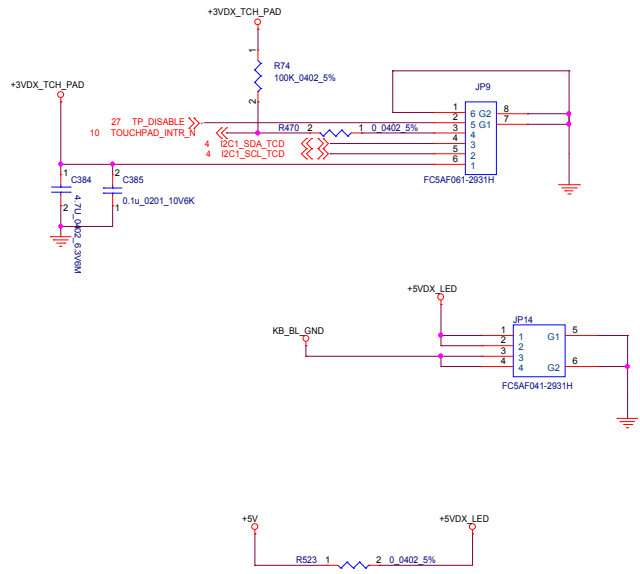


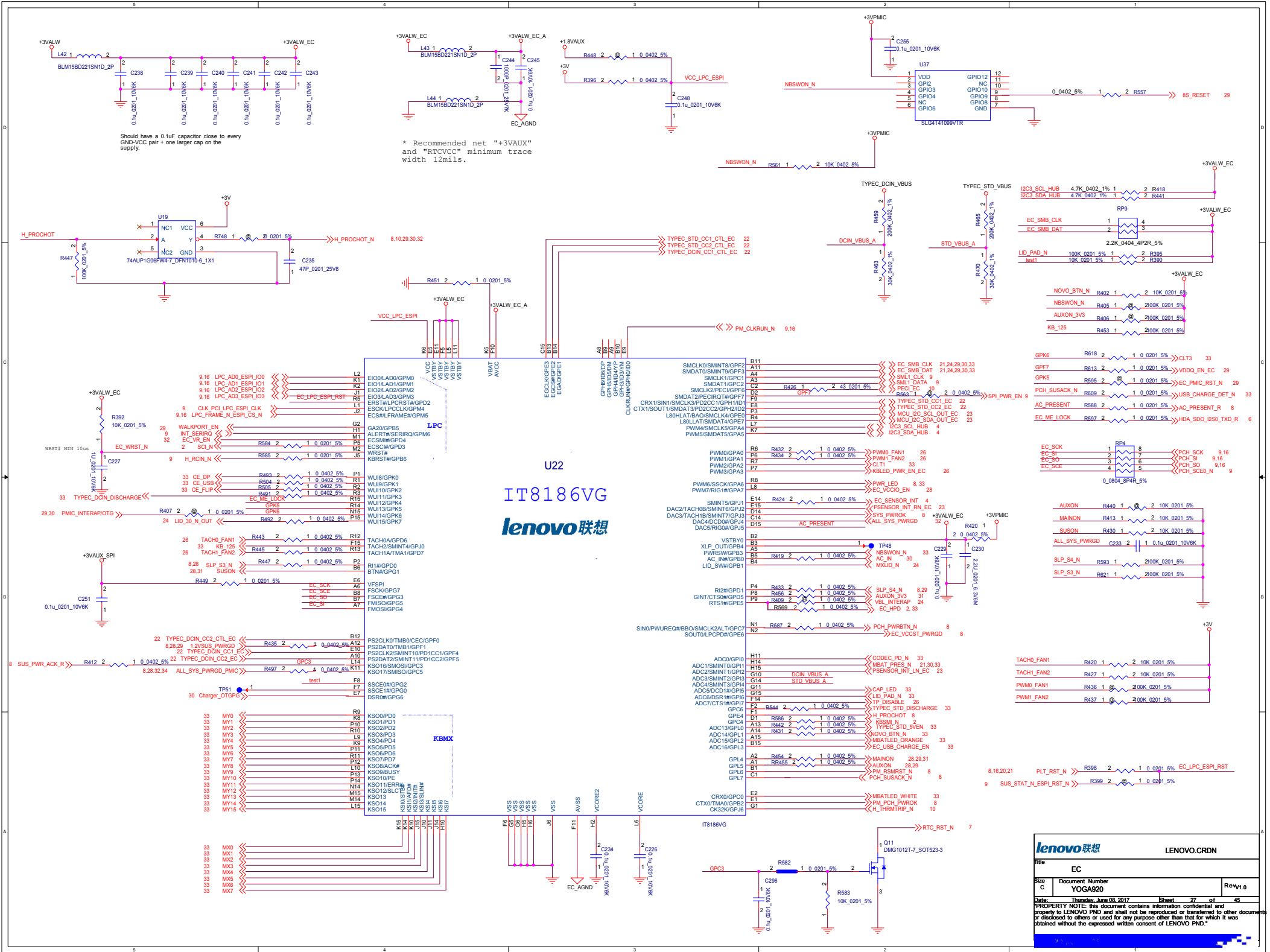


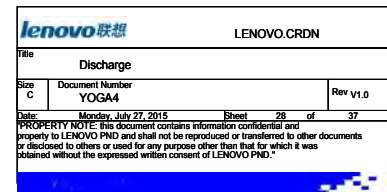
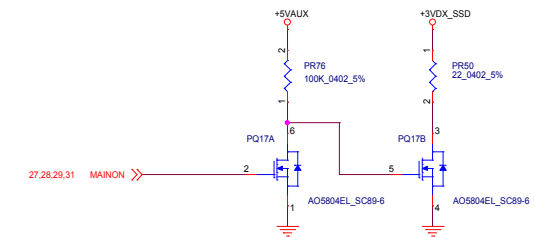
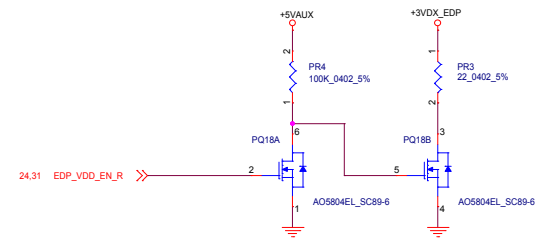
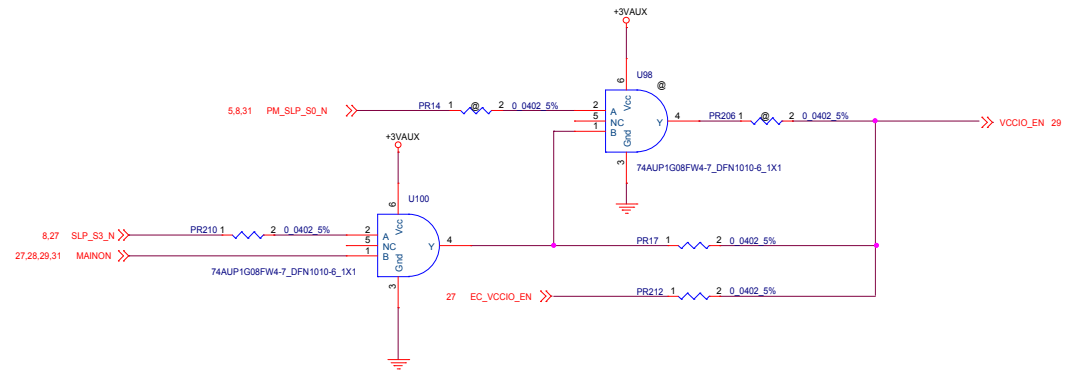
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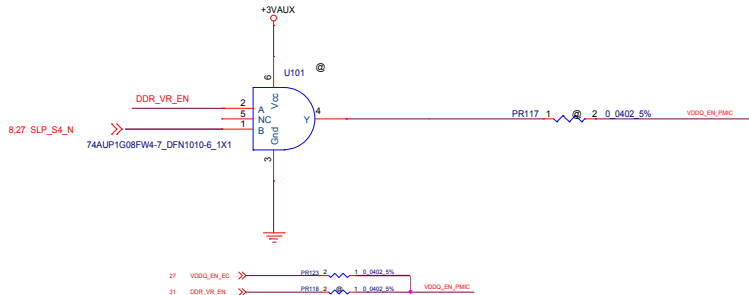
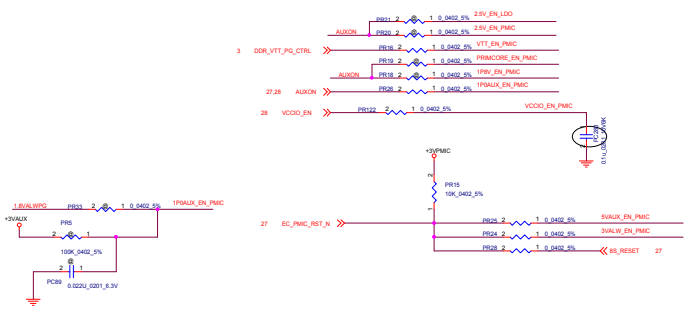
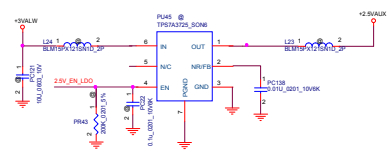
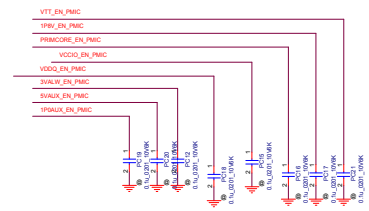
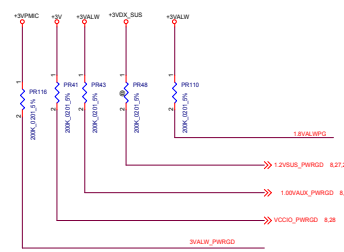
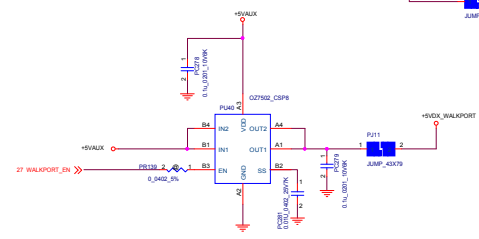
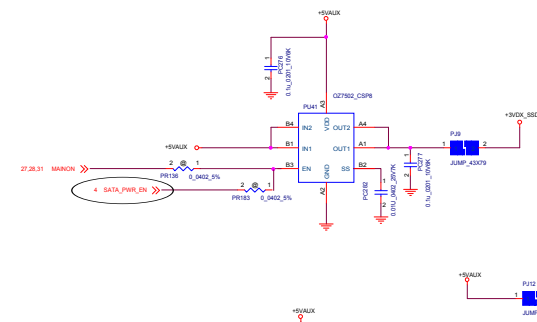






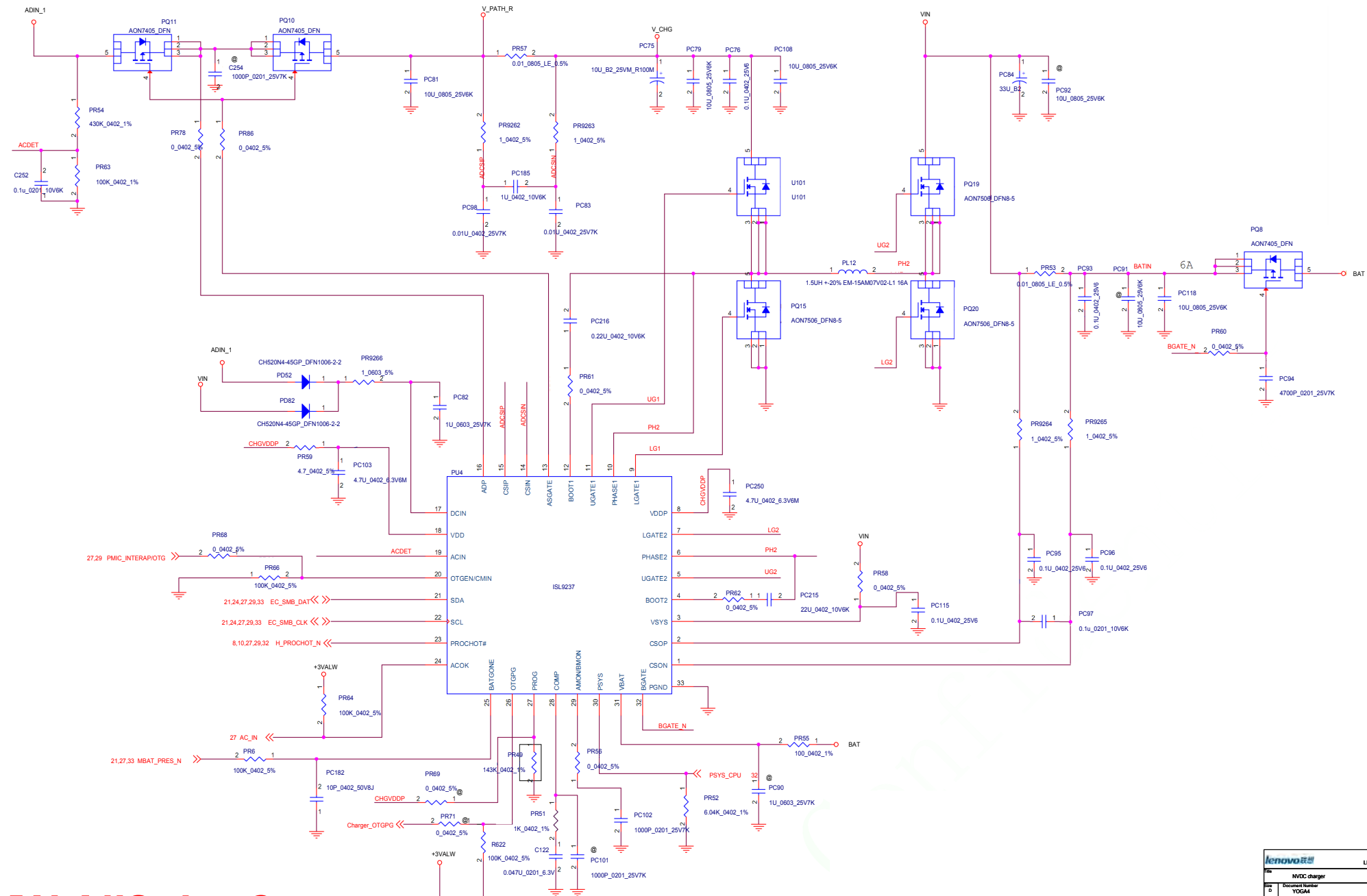


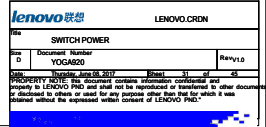


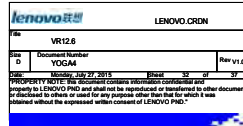


place close to J8 and J9

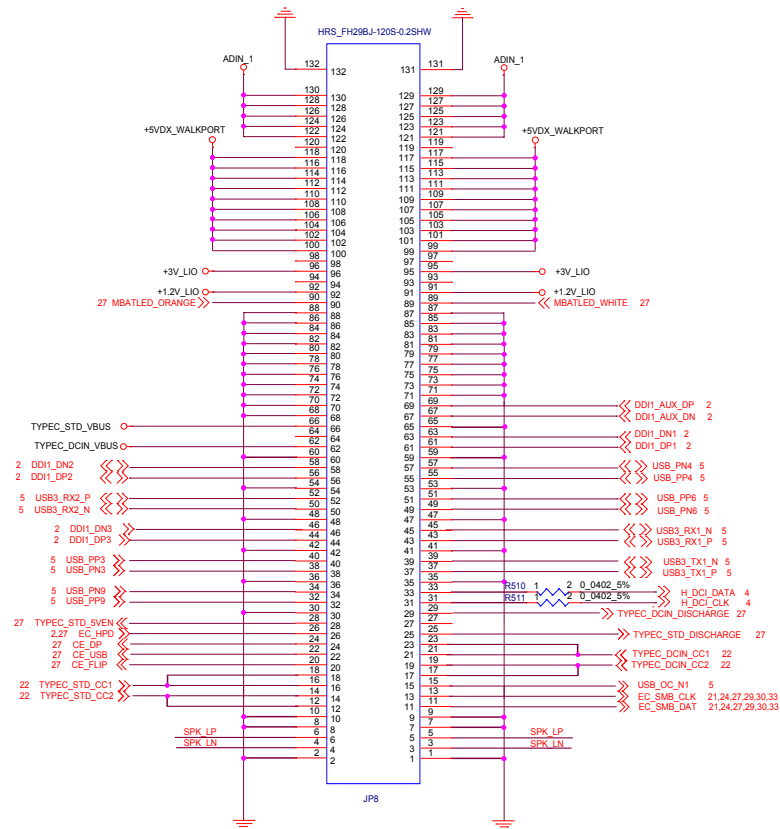
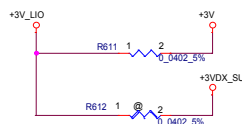
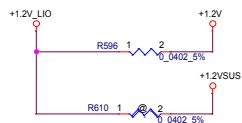
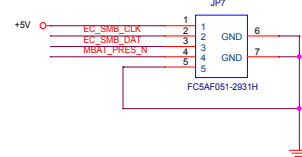
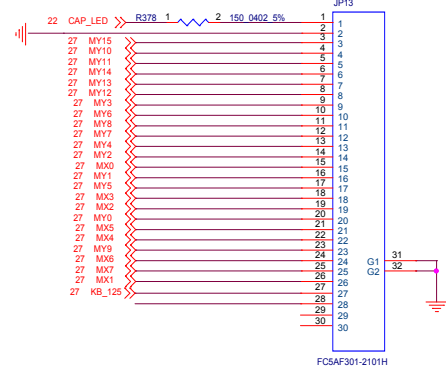
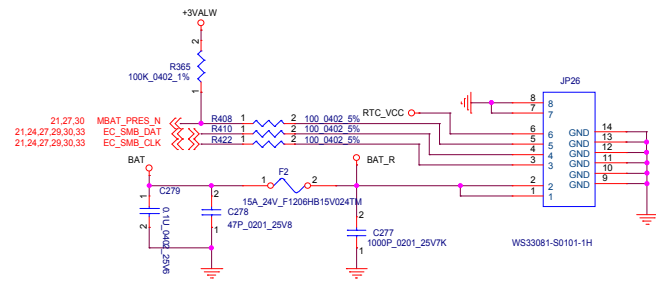
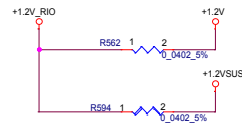
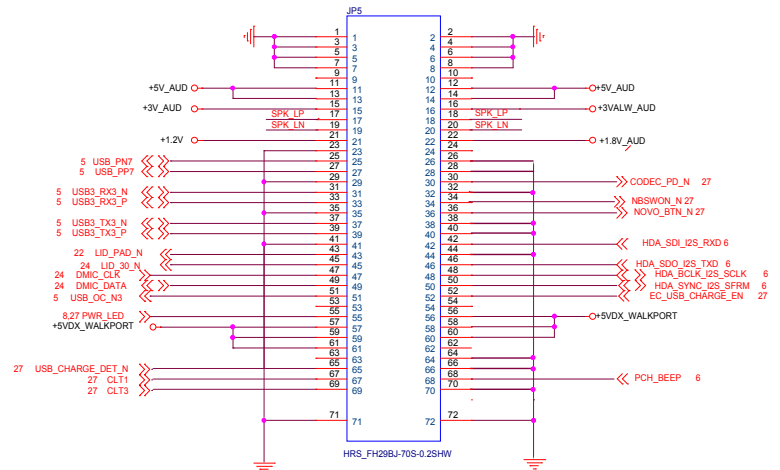
The diagram shows two resistors, PC1 and PC3, both labeled 100k. PC1 is connected to pin 2 and PC3 is connected to pin 32. Both pins are connected to ground. A VIN pin is also shown, connected to a 5V source.











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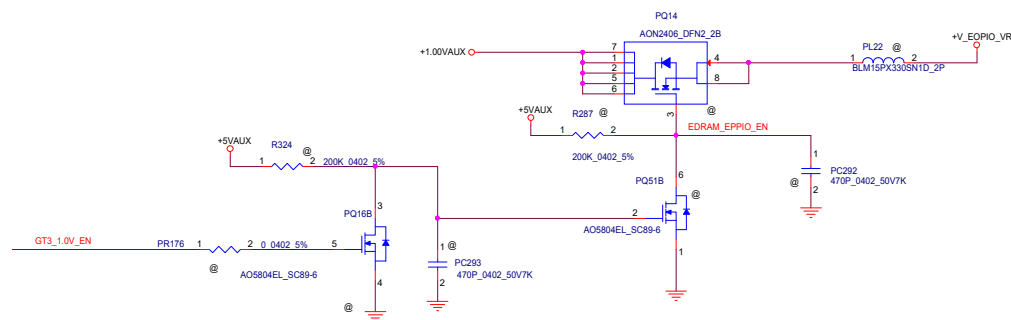
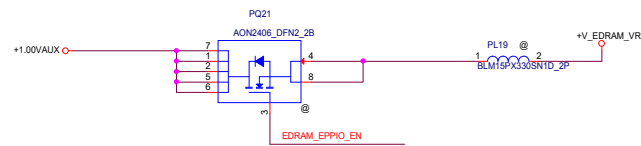
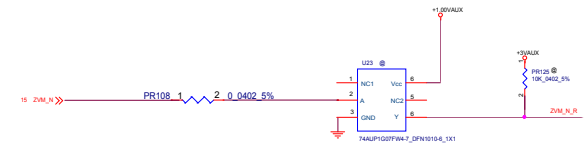
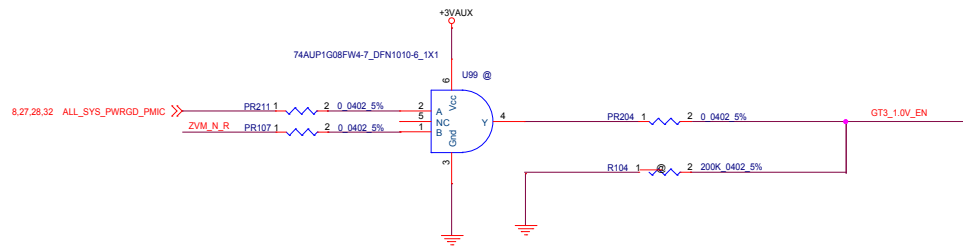
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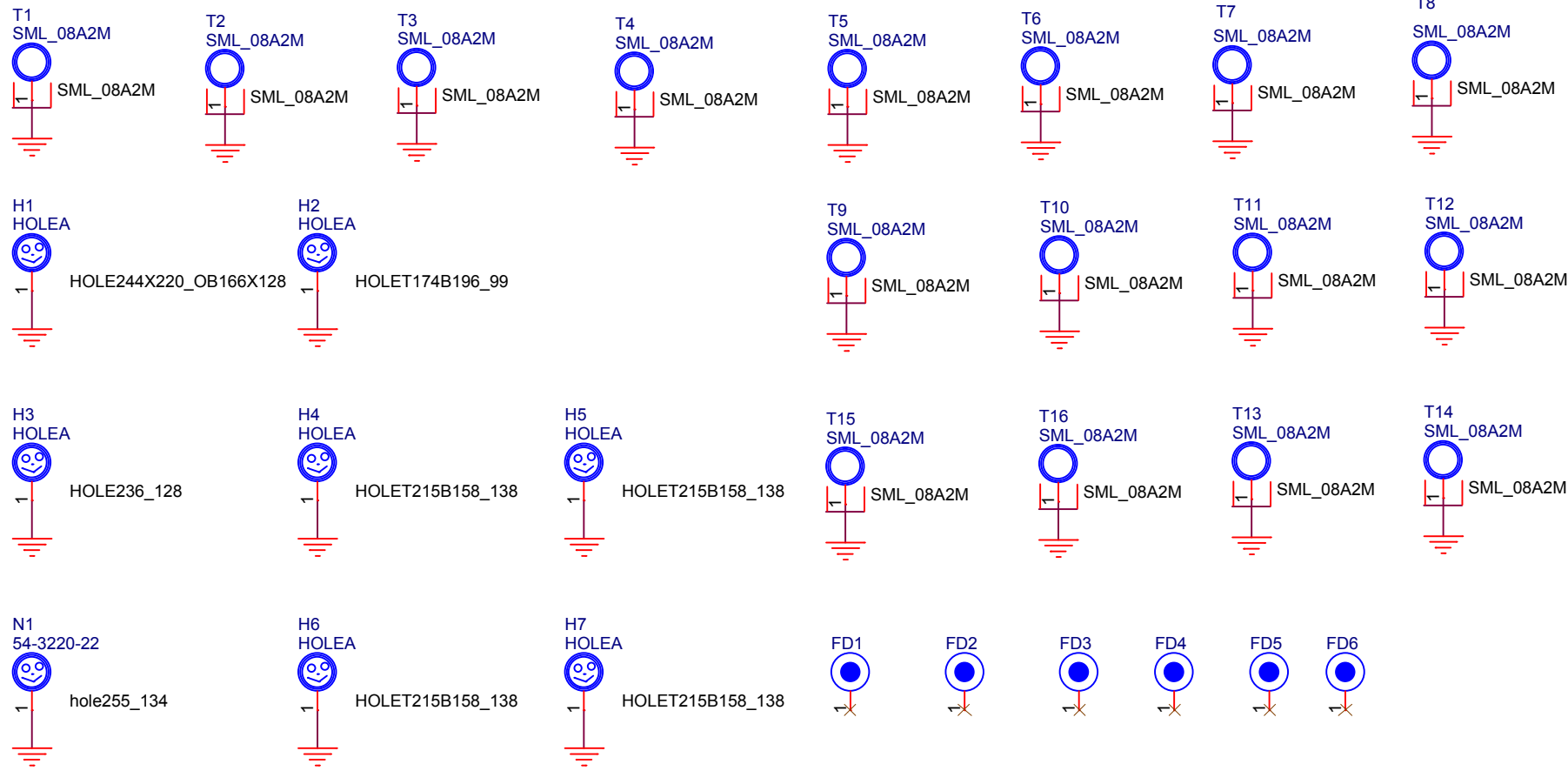
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